







































USBASP Programmer

File Buffer Chip



Chip **ATtiny13** Flash size **1 KB** EEPROM size **64 Bytes**

Flash EEPROM Lock and Fuse bits

Chip

Name

TINY13

Calibration 0

68

Lockbits

FF

Lockbit 21

11:No memory lock features enabled

Fusebits

32

Fusebit H

0:Enable serial programming

Fusebit G

0:Preserve EEPROM memory when chip is erased

Fusebit F

1:Watchdog timer always off

Fusebit E

1:Divide clock by 8, OFF

Fusebit DCBA

0010:Int. RC Osc. 9.6 MHz

Fusebits High

FF

Fusebit High 7

1:Selfprog disabled

Fusebit High 6

1:DEBUG WIRE disabled

Fusebit High 54

11:BOD disabled

Fusebit High 3

1:External reset enable

Refresh

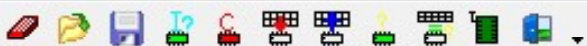
Write LB

Write FS

Write FSH

Write FSE

Write PRG

Chip **ATmega16A**Flash size **16 KB**EEPROM size **512 Bytes**

Flash EEPROM Lock and Fuse bits

Chip

Name MEGA16

Calibration 0 A2

Calibration 1 A2

Calibration 2 9D

Calibration 3 A0

Lockbits

FF

Lockbit 65 11:No restrictions for SPM or LPM accessing the boot loader

Lockbit 43 11:No restrictions for SPM or LPM accessing the application :

Lockbit 21 11:No memory lock features enabled for parallel and serial pro

Fusebits

EF

Fusebit C 1:BODLEVEL 2.7V

Fusebit B 1:BODEN disabled

Fusebit KLA987 101111:Ext. Crystal/Resonator High Freq.

Fusebits High DF

Fusebit High M 1:Disable OCD

Fusebit High J 1:Disable JTAG

Fusebit High I 0:SPI enabled

Fusebit High H 1:CKOPT 1

Fusebit High G 1:Erase EEPROM when chip erase

Fusebit High FE 11:128 Words boot size , F80

Fusebit High D 1:Reset vector is \$0000

Refresh

Write LB

Write FS

Write FSH

Write FSE

Write PRG